

## LoRaWAN DEVELOPMENT BOARD



### Description

The LM502 is a general LoRa Wireless Communication module, with integrated LoRa Radio Transceiver, SX1262 LoRa Modem and a 32-Bit RISC MCU CY8C4147AXI-S445 from Cypress. The MCU uses ARM Cortex M0+, with 48MHz operation frequency. The LoRa Radio Transceiver has continuous frequency coverage from 150MHz to 960MHz. The LoRa Modem supports LoRa modulation for LPWAN use cases and (G)FSK modulation for legacy use cases.

LM502 use the newest LoRa Modem SX1262 which provide high transmit power for ultra long range, ultra low power communication for LPWAN application.

LM502 can achieve a high sensitivity of over -140dBm and the maximum transmit power is higher than +21dBm. This makes it suitable to be used in long range LPWAN and have high efficiency.

LM502 is provided with ready to use LoRaWAN Modem software. Developers only need to use AT Commands to control the module so to join the LoRaWAN network. The LM502 also includes programmable and reconfigurable analog and digital blocks with flexible automatic routing. Developer can use the rich I/Os to connect to their sensors and provide a low cost / low power consumption / small size LoRaWAN End Node solution.

The LM502 demo board is a breakout board with LM502 pre-load. The demo board provides a rapid way to user to evaluate the feature of LM502. The demo board can be powered by 12v DC or USB port. The USB port of LM502 demo kit will be shown as one program port and one CDC port in computer, the program port is for flash firmware and CDC port is for serial access to LM502.

## General Specifications

- 48-MHz ARM Cortex-M0+ CPU
- LoRa Radio and LoRa Modem via SX1262
- 8-Channel DMA engine.
- Low power consumption
- Embedded 12-bit 1Msps SAR ADC
- SPI, 1xI2C, 2xUART, 1xSWD
- 3xADC, 1xCOMP.
- Baud rate configurable
- LoRa™ Modem
- Preamble detection
- Open source software
- Frequency: 865-867 MHz
- External Antenna via I-Pex connector
- ANT on SMD pad

## LoRa Specification

- LoRa Chip: sx1262
- 170 dB maximum link budget
- Max +22 dBm - 100 mW constant RF output
- Low RX current of 4.6 mA
- Programmable bit rate up to 62.5 kbps LoRa and 300 kbps FSK
- High sensitivity: down to -140 dBm
- Built-in bit synchronizer for clock recovery
- Automatic Channel Activity Detection (CAD) with ultra-fast AFC
- High Stability TCXO oscillator

## MCU Specification

- MCU: CY8C4147AXI-S445
- Flash:128KB
- SRAM:16KB
- Clock Speed: 48Mhz

## Absolute Maximum Ratings

- VCC: 0.3v ~ 3.9v
- Operating Temperature: -40 ~ 85°C
- I/O pins: 0.5v ~ VCC+0.5V
- RF Input Power: +10dBm

## Common DC Characteristics

- Supply Voltage: 1.8v ~ 3.7v
- Operating Temperature: -40 ~ 85°C
- I/O pins: Input High : > 0.7 x VDD, Input Low < 0.3 x VDD